



FOR IMMEDIATE RELEASE

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Press Release

Jetstream Media Technologies Announces All Security IP Cores Are Available in Both VHDL and Verilog

The broad product family which supports many cryptographic standards is now available in both VHDL and Verilog.

Naperville, Illinois, July 16, 2007 – Jetstream Media Technologies announced today the availability of all its security IP core in both native VHDL and native Verilog hardware description languages. The newly introduced native VHDL version of the entire IP family meets the demand from many geographic areas and customers needs.

The support of both native VHDL code and native Verilog code of all its IP cores allows Jetstream Media Technologies' product line with a global reach.

The security IP family supports many cryptographic standards including AES, AES-CCM, AES-GCM, XEX-AES, secure hashing algorithms, HMAC, and Triple-DES. It also has a wide selections of speed area trade-off from small module of 8-bit data path to fully pipelined ultra-high throughput cryptographic engine.

Jetstream Media Technologies' security IP products have proven to be at the highest quality. They have recently passed National Institute of Science and Technology (NIST) cryptographic algorithm validation program (CAVP), a strict governmental testing program for the validation of cryptographic accuracy.

For additional information please contact us, or visit our web site:

www.security-cores.com
or
www.jetsmt.com

About Jetstream Media Technologies Corp

Jetstream Media Technologies is a rapidly growing company in the field of semiconductor intellectual property (IP) cores for protection of communications, storage and digital contents.